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## U.S. UTILITY Patent Application

O.I.P.E.  
SCANNED Q.A.

**PATENT DATE**

APPLICATION NO. 09/780634	CONT/PRIOR	CLASS 713	SUBCLASS 323	ART UNIT 2182	EXAMINER Aure
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## APPLICANTS

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**TITLE**

Power control of a processor using hardware structures controlled by a compiler with an accumulated instruction profile

PTO-2040  
12/89

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>  _____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____  _____  _____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>  Amount Due Date Paid	
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